

CLAIMS

1. A digital-to-analog converter for converting a digital value into an analog quantity, said converter comprising current sources switched as a function of said digital value in order to generate an output current reflecting the value of said analog quantity, said converter comprising means for generating a correction current added to said output current, said correction current comprising a component proportional to the square of said output current.
2. A converter as claimed in Claim 1 further comprising a differential amplifying stage situated upstream of said generation means, said attenuating stage delivering to said generation means an output signal proportional to said analog quantity.
3. A converter as claimed in Claim 2 wherein the differential amplifying stage comprises a capacitive element for increasing the amplification factor as a function of frequency.
4. A converter as claimed in one of Claims 1 to 3 wherein the generation means comprise a Gilbert cell for generating said correction current.
5. An integrated circuit comprising a digital-to-analog converter for converting a digital value into an analog quantity, said converter comprising current sources switched as a function of said digital value in order to generate an output current reflecting the value of said analog quantity, the converter comprising means for generating a correction current added to said output current, said correction current comprising a component proportional to the square of said output current.